



FORM PTO-1449 (Rev. 2-32) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	SERIAL NO. 10/038,868	DOCKET NO. 3902 US
	APPLICANTS DEAS, Alexander, Roger ABROSIMOV, Igor Anatolievich	
	FILING DATE January 8, 2002	

RECEIVED

MAR 04 2003

Technology Center 2100

U.S. PATENT DOCUMENTS							
Examiner Initial	Cite No.	Document Number	Date MM-DD-YYYY	Name of Patentee or Applicant	Class	Subclass	Filing date if appropriate
CW	1	6,127,864	10-03-2000	Mission Research Corporation	327	144	
CW	2	5,018,142	05-21-1991	Digital Equipment Corporation	370	112	
CW	3	4,965,884	10-23-1990	Northern Telecom Limited	375	106	
CW	4	4,891,812	01-02-1990	Motorola, Inc.	371	5.5	
CW	5	4,771,421	09-13-1988	Telefonaktiebolaget LM Ericsson	370	85	
CW	6	4,432,094	02-14-1984	Siemens Aktiengesellschaft	375	94	
CW	7	4,241,311	12-23-1980	Telex Computer Products, N.C.	328	167	
CW	8	6,374,361	04-16-2002	Silicon Image, Inc.	713	503	
CW	9	6,229,859	05-08-2001	Silicon Image, Inc.	375	354	
CW	10	5,905,769	05-18-1991	Silicon Image, Inc.	375	376	
CW	11	2002/0085656 A1	07-04-2002	Sang-Hyun Lee et al.	375	355	
CW	12	2001/0031028 A1	10-18-2001	Cicero Silveira Vaucher	375	355	

FOREIGN PATENT DOCUMENTS							
Examiner	Cite No.	Document Number	Date MM-DD-YYYY	Country	Class	Subclass	Translation YES/NO
CW	1	EP 0797326 A2	09-24-1997	DE, FR, GB, IT, NL	H04L 1	00	
CW	2	EP 0575000 A1	12-22-1993	CH, DE, FR, GB, IT, LI	H04L 25	38	
CW	3	EP 0193332 A2	09-03-1986	DE, FR, GB, NL	H04L 1	08	

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)			
CW	1	A 5-Gb/s 0.25 μ m CMOS Jitter-Tolerant Variable-Interval Oversampling Cock/Data Recovery Circuit by Sang-Hyun Lee, et al., IEEE Journal of Solid - State Circuits, Vol/ 37, No. 12, December 2002	

EXAMINER <i>Cicely</i>	DATE CONSIDERED 7/5/05
----------------------------------	----------------------------------

FORM PTO-1449 (Rev. 2-32)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			SERIAL NO.		DOCKET NO. 4221CIP	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)					APPLICANT ABROSIMOV, Igor Anatolievich			
					FILING DATE			
U.S. PATENT DOCUMENTS								
Examiner Initial	Cite No.	Document Number	Date	Name of Patentee or Applicant	Class	Subclass	Filing date if appropriate	
<i>[Signature]</i>		4 486 739 A	04-Dec-1984	FRANASZEK	340	347		
		4 376 309 A	03-Mar-1983	FENDERSON	375	101		
FOREIGN PATENT DOCUMENTS								
Examiner	Cite No.	Document Number	Date	Country	Class	Subclass	Translation YES/NO	
<i>[Signature]</i>		0 493 044 A3	1-Jul-1992	EP	H03M	7/14		
OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)								
EXAMINER	<i>[Signature]</i>				DATE CONSIDERED 9/15/95			